INTEL CORP Form 4 July 28, 2016

FORM 4

UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

Check this box

if no longer subject to

Section 16. Form 4 or Form 5 obligations may continue.

See Instruction

STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF **SECURITIES**

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934, Section 17(a) of the Public Utility Holding Company Act of 1935 or Section 30(h) of the Investment Company Act of 1940

1(b).

(Print or Type Responses)

1. Name and Address of Reporting Person * PLUMMER JAMES D

2. Issuer Name and Ticker or Trading Symbol

INTEL CORP [INTC]

(Last) (First) (Middle)

3. Date of Earliest Transaction (Month/Day/Year)

CISX ROOM 330 VIA PALOU MALL, STANFORD UNIVERSITY

(Street) 4. If Amendment, Date Original

Filed(Month/Day/Year)

07/26/2016

5. Relationship of Reporting Person(s) to

OMB

Number:

Expires:

response...

OMB APPROVAL

Estimated average

burden hours per

3235-0287

January 31,

2005

0.5

Issuer

(Check all applicable)

_X__ Director 10% Owner Other (specify Officer (give title below)

6. Individual or Joint/Group Filing(Check

Applicable Line)

X Form filed by One Reporting Person Form filed by More than One Reporting

STANFORD, CA 94305

(City)	(State) (Z	Zip) Table	e I - Non-Do	erivative S	Securi	ties Ac	quired, Disposed	of, or Beneficia	lly Owned
1.Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)	4. Securities ionAcquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		5. Amount of Securities Beneficially Owned Following	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)	
			Code V	Amount	(A) or (D)	Price	Reported Transaction(s) (Instr. 3 and 4)		
Common Stock	07/26/2016		M	1,588 (1)	A	\$0	24,863	D	
Common Stock	07/26/2016		M	4,258 (1)	A	\$0	29,121	D	
Common Stock							27,835	I	By Family Trust

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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number.

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)	5. Number on Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4, and 5)	6. Date Exercisab Expiration Date (Month/Day/Year		7. Title and A Underlying So (Instr. 3 and 4
				Code V	(A) (D)	Date Exercisable	Expiration Date	Title
Restricted Stock Units	\$ 0 (2)	07/26/2016		M	1,588	07/26/2014(3)	<u>(3)</u>	Common Stock
Performance-based Restricted Stock Units	\$ 0 (4)	07/26/2016		M	4,190	07/26/2016(5)	(5)	Common Stock

Reporting Owners

Reporting Owner Name / Address	Relationships					
	Director	10% Owner	Officer	Other		
PLUMMER JAMES D CISX ROOM 330 VIA PALOU MALL STANFORD UNIVERSITY STANFORD, CA 94305	X					

Signatures

/s/ Fernando Delmendo, attorney-in-fact 07/28/2016

**Signature of Reporting Person Date

Explanation of Responses:

- * If the form is filed by more than one reporting person, see Instruction 4(b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- (1) Shares acquired on the vesting of restricted stock units.
- (2) Each restricted stock unit represents the right to receive, following vesting, one share of Intel Corporation common stock.
- (3) Unless earlier forfeited under the terms of the RSU, 33 1/3% of the awards vest and convert into common stock on each anniversary of the grant date, unless that date falls on a non-business date, in which case the next business date shall apply.
- (4) Each Performance-based Restricted Stock Unit (RSU) represents the right to receive, following vesting, no less than 50% and no more than 200% of one share of Intel common stock, together with dividend equivalent shares on the vested number of shares. The resulting number of shares of Intel common stock acquired upon vesting of the Performance-based RSUs is contingent upon the achievement of

Reporting Owners 2

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pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year period beginning January 24, 2013 and ending on January 24, 2016, unless that date falls on a date that the NASDAQ Stock Market is closed, in which case the next business date that the NASDAQ Stock Market is open shall apply.

- Unless earlier forfeited under the terms of the Performance-based RSU, each Performance-based RSU vests and converts into no less than 50% and no more than 200% of one share of Intel common stock three years after the grant date (together with dividend equivalent shares thereon), unless that date falls on a non-business date, in which case the next business date shall apply.
 - Due to a limitation in the filing software, the total number of shares of Common Stock received was 4,258, as reported above in Table I.
- (6) The conversion rate of the Performance-based Restricted Stock was 92.853%. Upon conversion of the RSUs, the Reporting Person received 3,890 shares of Common Stock. Upon conversion of \$11,008.70 in dividend equivalents, the Reporting person received an additional 368 shares of Common Stock.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure. Potential persons who are to respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB number.