

PERLMUTTER DAVID
Form 4
January 28, 2013

FORM 4

**UNITED STATES SECURITIES AND EXCHANGE COMMISSION
Washington, D.C. 20549**

OMB APPROVAL

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STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934, Section 17(a) of the Public Utility Holding Company Act of 1935 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Responses)

1. Name and Address of Reporting Person *
PERLMUTTER DAVID

(Last) (First) (Middle)
INTEL CORPORATION, 2200 MISSION COLLEGE BLVD.
(Street)

SANTA CLARA, CA 95054

(City) (State) (Zip)

2. Issuer Name and Ticker or Trading Symbol
INTEL CORP [INTC]

3. Date of Earliest Transaction (Month/Day/Year)
01/24/2013

4. If Amendment, Date Original Filed (Month/Day/Year)

5. Relationship of Reporting Person(s) to Issuer

(Check all applicable)

____ Director _____ 10% Owner
 Officer (give title below) _____ Other (specify below)
EXEC VP; GM IAG; CPO

6. Individual or Joint/Group Filing (Check Applicable Line)
 Form filed by One Reporting Person
 Form filed by More than One Reporting Person

Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)	4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)	5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Ownership (Instr. 4)
			Code	V Amount (A) or (D) Price			
Common Stock	01/24/2013		M	4,837 (1) A \$ 0	411,553.2604 (2)	D	
Common Stock	01/24/2013		M	4,199 (1) A \$ 0	415,752.2604	D	

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned
(e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)	5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4, and 5)	6. Date Exercisable and Expiration Date (Month/Day/Year)	7. Title of Underlying Security (Instr. 3)
Restricted Stock Units	\$ 0 ⁽³⁾	01/24/2013		M	4,837	04/24/2011 ⁽⁴⁾ ⁽⁴⁾	Common Stock
Restricted Stock Units	\$ 0 ⁽³⁾	01/24/2013		M	4,199	04/24/2012 ⁽⁵⁾ ⁽⁵⁾	Common Stock
Performance-based Restricted Stock Units	\$ 0 ⁽⁶⁾	01/24/2013		A	85,160	02/24/2016 ⁽⁷⁾ ⁽⁷⁾	Common Stock
Employee Stock Option (Right to Buy)	\$ 21.055	01/24/2013		A	322,580	01/24/2014 ⁽⁸⁾ 01/24/2020	Common Stock
Restricted Stock Units	\$ 0 ⁽³⁾	01/24/2013		A	64,120	04/24/2013 ⁽⁹⁾ ⁽⁹⁾	Common Stock

Reporting Owners

Reporting Owner Name / Address	Relationships			
	Director	10% Owner	Officer	Other
PERLMUTTER DAVID INTEL CORPORATION 2200 MISSION COLLEGE BLVD. SANTA CLARA, CA 95054			EXEC VP; GM IAG; CPO	

Signatures

/s/ Wendy Yemington,
attorney-in-fact

01/28/2013

__Signature of Reporting Person

Date

Explanation of Responses:

- * If the form is filed by more than one reporting person, see Instruction 4(b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- (1) Shares acquired on the vesting of restricted stock units.
- (2) Includes 1.8478 shares acquired under the Intel Corporation Direct Stock Purchase Plan and Dividend Reinvestment Plan in December 2012.
- (3) Each restricted stock unit represents the right to receive, following vesting, one share of Intel Corporation common stock.

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(4) Unless earlier forfeited under the terms of the RSU, 1/12th of the award vests and converts into common stock in twelve substantially equal quarterly tranches, beginning on April 24, 2011. If the quarterly vesting date falls on a non-business date, the next business date shall apply.

(5) Unless earlier forfeited under the terms of the RSU, 1/12th of the award vests and converts into common stock in twelve substantially equal quarterly tranches, beginning on April 24, 2012. If the quarterly vesting date falls on a non-business date, the next business date shall apply.

(6) Each Performance-based Restricted Stock Unit (RSU) represents the right to receive, following vesting, no less than 50% and no more than 200% of one share of Intel common stock, together with dividend equivalent shares on the vested number of shares. The resulting number of shares of Intel common stock acquired upon vesting of the Performance-based RSUs is contingent upon the achievement of pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year period beginning on the grant date and ending on the third anniversary of the grant date, unless that date falls on a date that the NASDAQ Stock Market is closed, in which case the next business date that the NASDAQ Stock Market is open shall apply.

(7) Unless earlier forfeited under the terms of the Performance-based RSU, each Performance-based RSU vests and converts into no less than 50% and no more than 200% of one share of Intel common stock three years and one month after the grant date (together with dividend equivalent shares thereon), unless that date falls on a non-business date, in which case the next business date shall apply.

(8) Unless earlier forfeited under the terms of the option, the option vests in four equal annual installments beginning on the first anniversary of the grant date, unless that date falls on a non-business date, in which case the next business date shall apply.

(9) Unless earlier forfeited under the terms of the RSU, 1/12th of the award vests and converts into common stock in twelve substantially equal quarterly tranches, beginning on April 24, 2013. If the quarterly vesting date falls on a non-business date, the next business date shall apply.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure.

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